Computers are full of binary registers and counters. This is an attempt to construct a 4-bit binary counter with electromechanical relays. It also displays the resulting 4-bit binary number on a regular 7-segment display.



followed by $\mathbf{P}, \mathbf{b}, \dots, \mathbf{F}$.

These little circuit boards with LEDs show the status of the 1-bit counters.





These 5 relays implement a demultiplexer. This turns the 4-bit binary signal into 16 discrete signals, to drive the diode matrix for the 7segment display.

An old 24v DC power supply, once used for a piece of computer equipment, powers the device.



These groups of 3 relays implement a 1-bit binary counter.



This 7-segment display displays the hexadecimal position of the counter.

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This is an old timer relay that takes care of the pulses that drive the counter.

To avoid using an integrated circuit for displaying the 4-bit result on a 7-segment display, I build a diode matrix. This drives the 7-segment display, based on the counter position.

